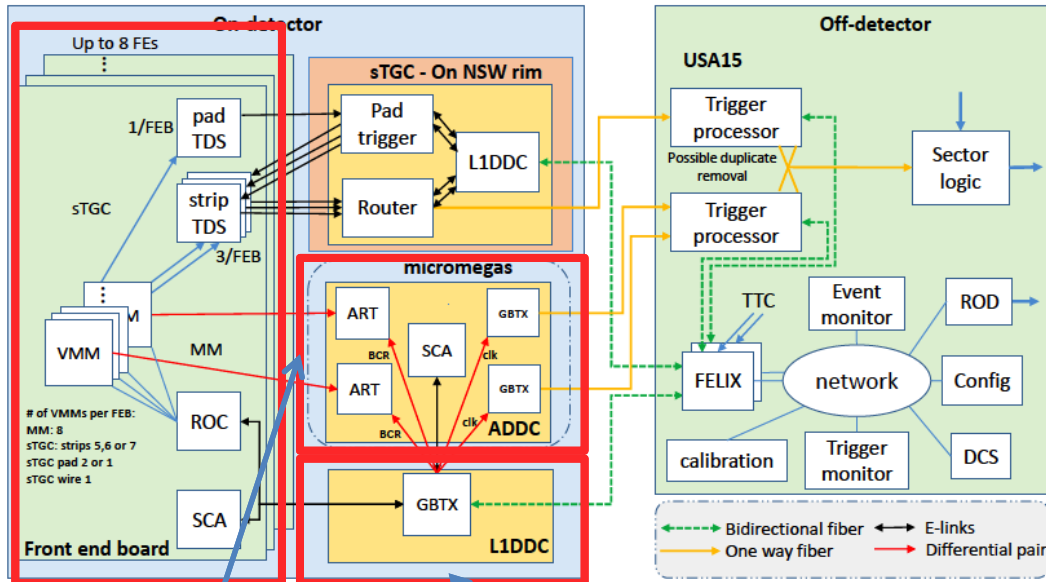


Overall readout and trigger Scheme

Micromegas Detector

FE card



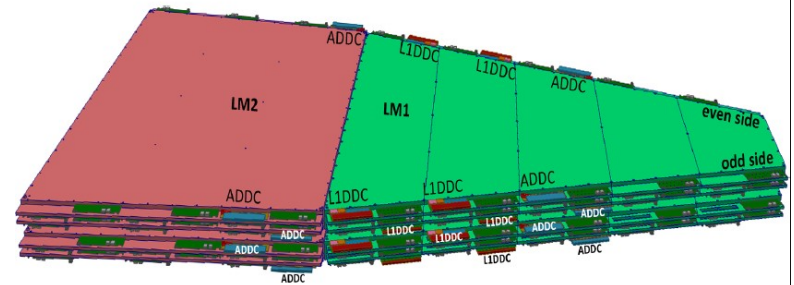
26/09/2017

NSW ADDC/L1DDC FDR, FEBs IDR

Trigger stream

Data stream

NSW MM FE Electronics - Placement

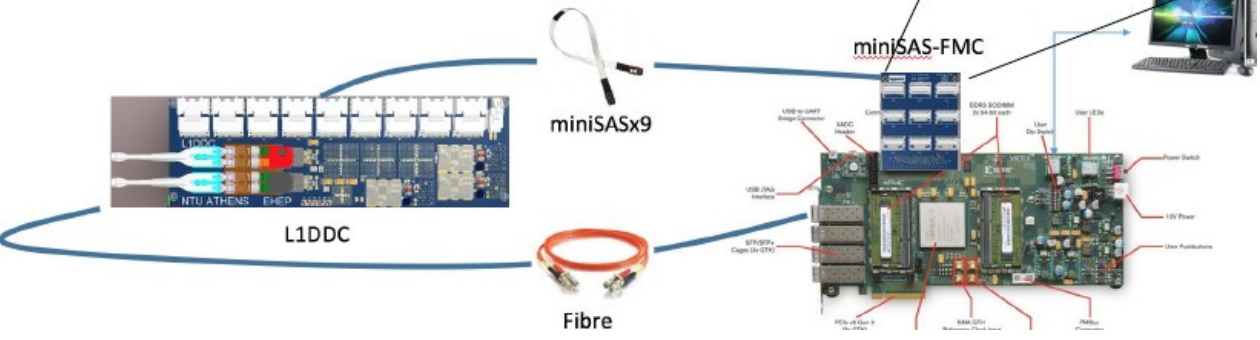
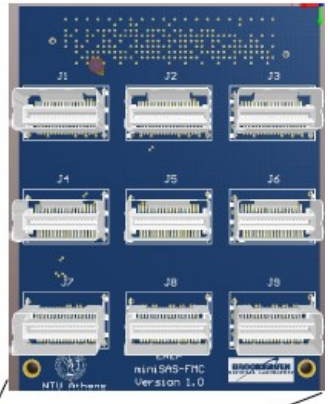


8 MMFE8, 1 L1DDC, 1 ADDC per plane per side; in total: 4096 MMFE8s, 512 L1DDC, 512 ADDCs



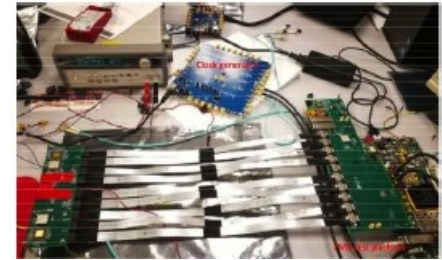
L1DDC Testing

- 1) Send data through 9 miniSAS connectors and receive Output via fiber
- 2) Test bench consists of
 - i) VC709 evaluation boards
 - ii) FMC card with 9 miniSAS
 - iii) Fibers and miniSAS cables
 - iv) PC workstation



ADDC Testing

- 1) Closed loop with mezzanine and VC707
- 2) Test bench consists of
 - i) VC707 board
 - ii) FMC card
 - iii) Fibers/miniSAS cables
 - iv) PC workstation



L1DDC setup at Demokritos

